Notice of Allowability	Application No.	Applicant(s)
	10/626,256	VOICU ET AL.
	Examiner	Art Unit
	Tan T. Nguyen	2818
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this a or other appropriate communication IGHTS. This application is subject	application. If not included on will be mailed in due course. THIS
1. This communication is responsive to the application filed o	on July 23, 2003.	
2. The allowed claim(s) is/are <u>1-31</u> .		
3. The drawings filed on are accepted by the Examine	r.	
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. ☐ Interview Summa Paper No./Mail D 08), 7. ☑ Examiner's Amen	Date

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- a. Authorization for this examiner's amendment was given in a telephone interview with Mr. Eric Hoffman on October 19, 2004.
- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

In claim 1, line 5, after the word "circuit", a --;-- has been inserted.

3. The following is an examiner's statement of reasons for allowance:

The closest prior art to the present invention is Senda et al. (U.S. Patent No. 6,584,013). Senda et al. disclosed a memory device having a power supply terminal and a terminal coupled to ground, and a plurality of terminals for data, address and other control signals. Senda et al. failed to show or suggest the limitation of a power pin through which a nominal voltage is received to power a programming circuit, and through which a series of high voltage pulses are received; a data pin through which a data input of each of the non-volatile memory cells receives binary data, and through which a programming voltage input of each of the non-volatile memory cells receives a high voltage pulse.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tan T. Nguyen whose telephone number is (571) 272-1789. The examiner can normally be reached on Monday to Friday from 07:00 AM to 03:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms, can be reached at (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tan T. Nguyen Primary Examiner Art Unit 2818 October 19, 2004